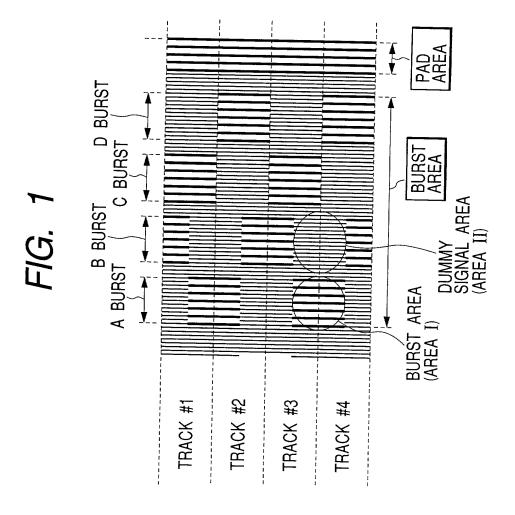
1 / 17



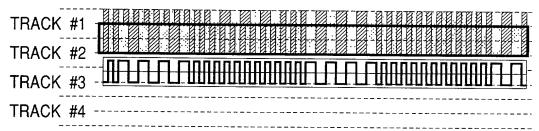
2/17

POSITION C BURST / BURST AREA B BURST A BURST TRACK #1 TRACK #2 TRACK #3 TRACK#4

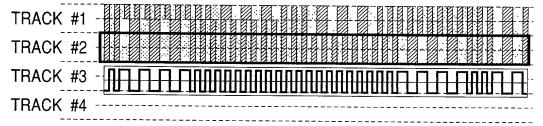
#### FIG. 3(a)



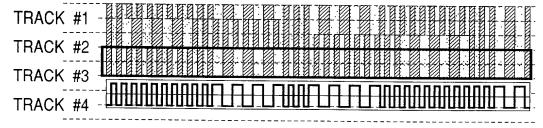
### FIG. 3(b)



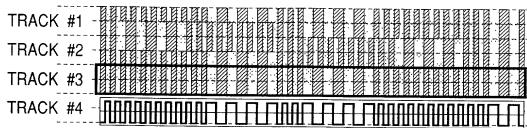
### FIG. 3(c)



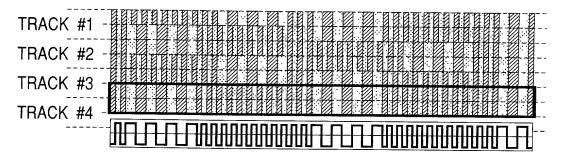
# FIG. 3(d)



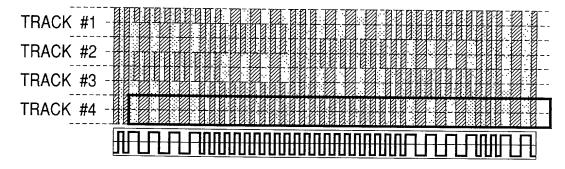
# FIG. 3(e)



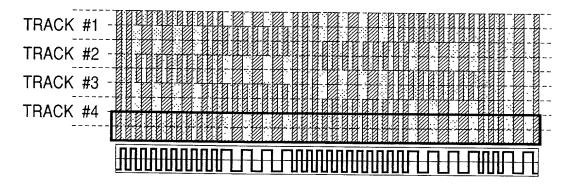
## FIG. 4(f)



# FIG. 4(g)



### FIG. 4(h)



### FIG. 4(i)

TRACK #2	 -
	я-·
TRACK #3	<u>-</u> -
TRACK #4 - 1	1 1

FIG. 5

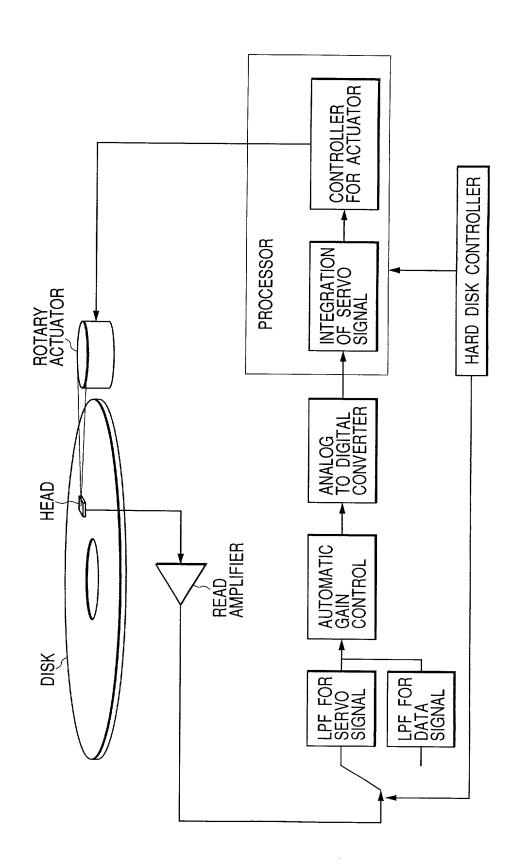


FIG. 6

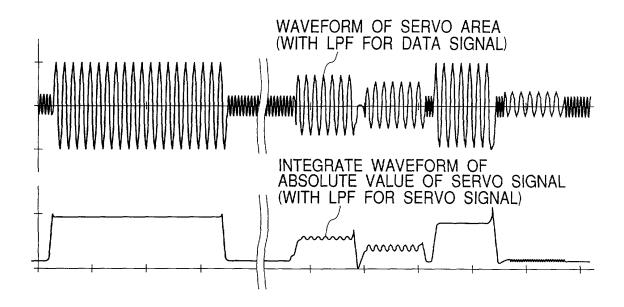
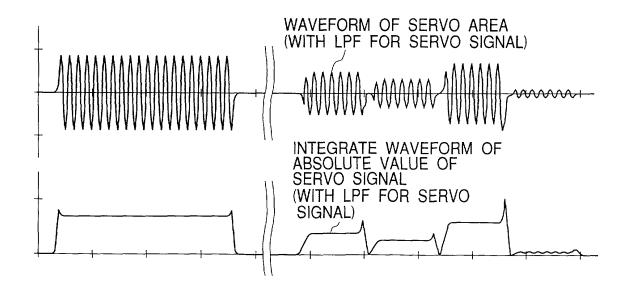


FIG. 7



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**PROCESSOR** H AUTOMATIC GAIN CONTROL DISK F

FIG. 9

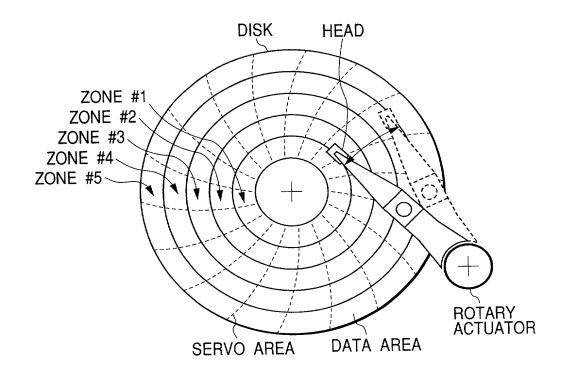


FIG. 10

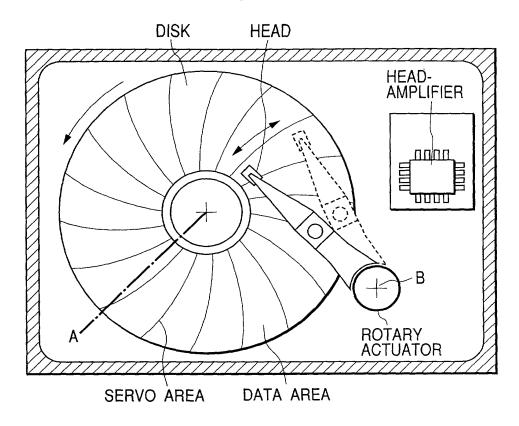


FIG. 11

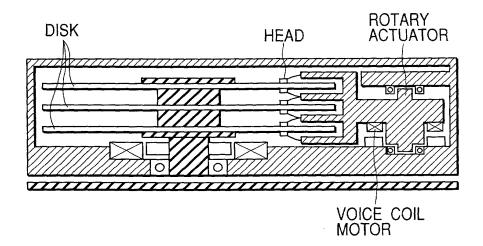


FIG. 12

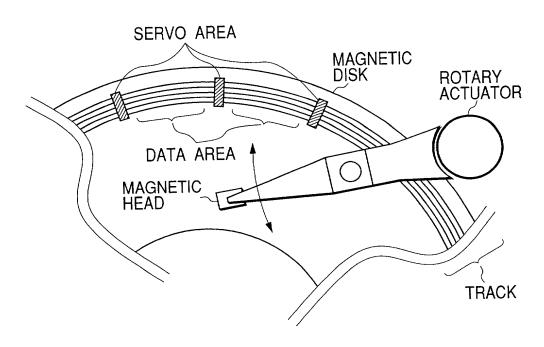


FIG. 13

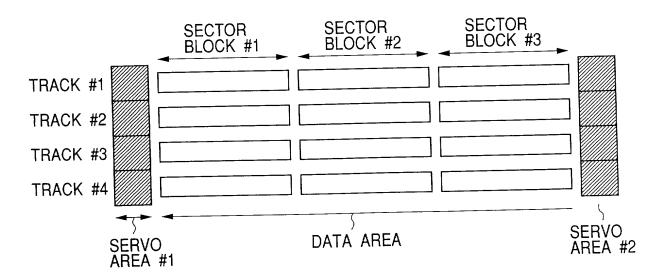


FIG. 14

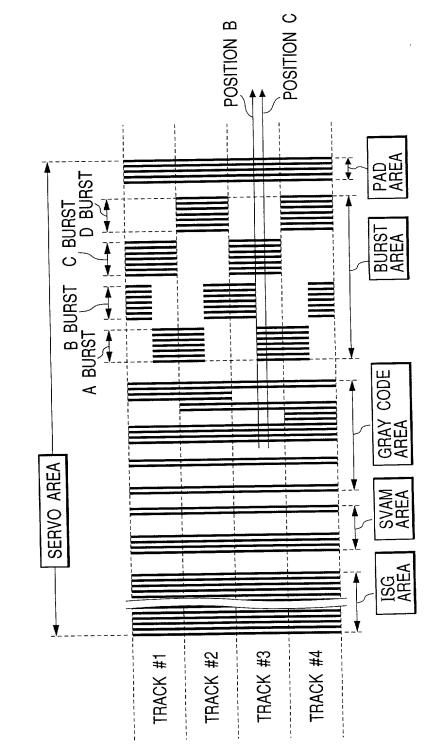


FIG. 15

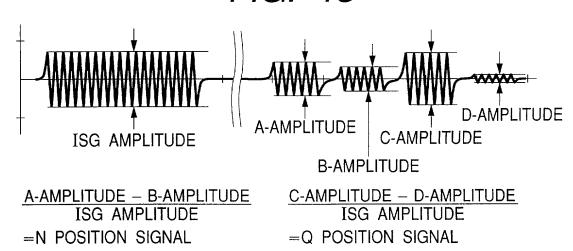
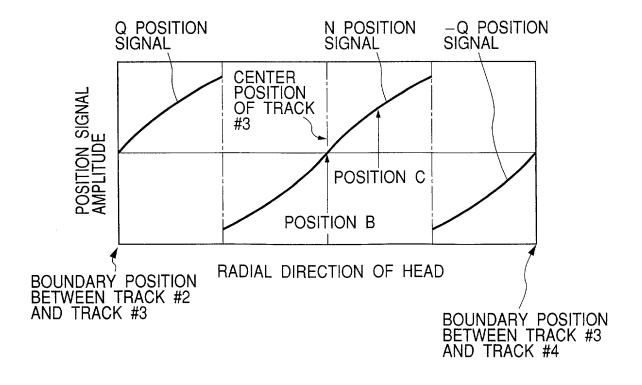
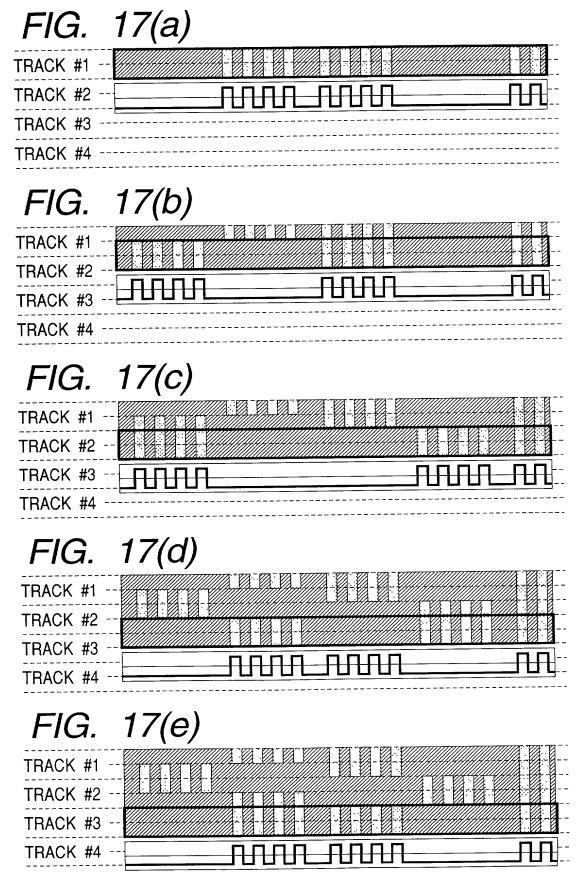
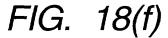
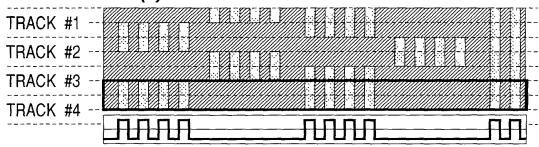


FIG. 16

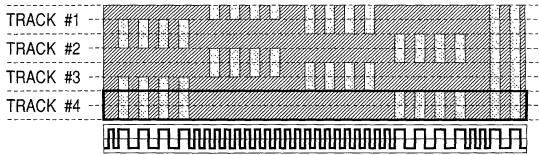




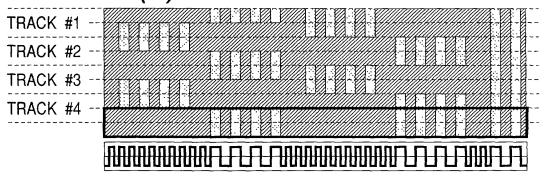




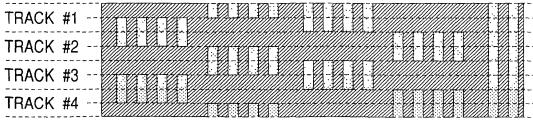
### FIG. 18(g)

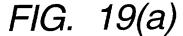


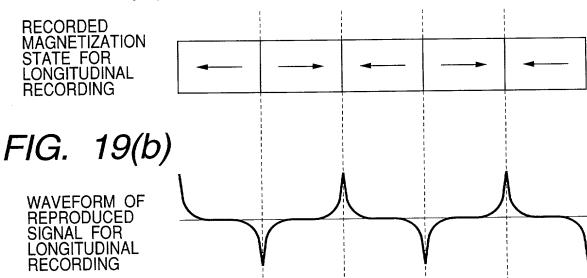
# FIG. 18(h)



### FIG. 18(i)







#### FIG. 19(c)

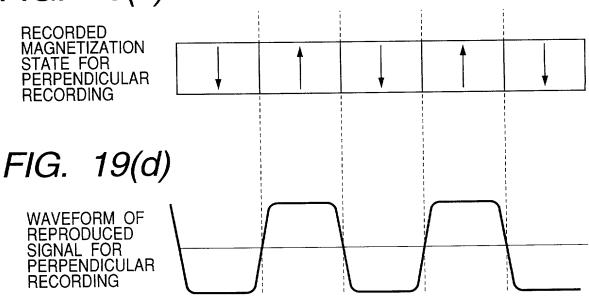


FIG. 20(a)

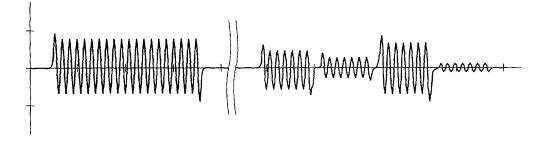


FIG. 20(b)

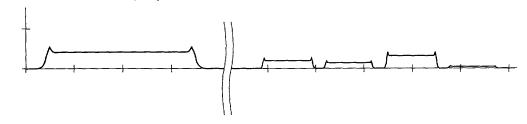


FIG. 21(a)

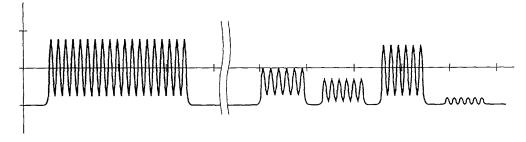


FIG. 21(b)

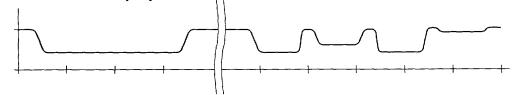


FIG. 22

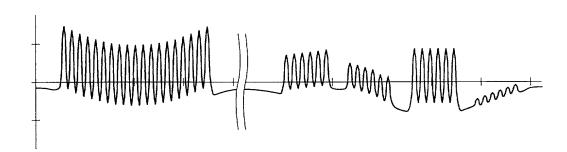


FIG. 23

